

APPLICANT

Serial No.

10/076920  
02/15/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10076920	02/15/2002	209		3653	

\*\*APPLICANTS: Deliwala Shrenik;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/991,542 11/10/2001  
WHICH IS A CIP OF 09/859,693 05/17/2001  
AND CLAIMS BENEFIT OF 60/293,615 05/25/2001  
AND CLAIMS BENEFIT OF 60/297,208 06/08/2001

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 53168-500301D3
TITLE : Integrated optical/electronic circuits and associated methods of simultaneous generation thereof		
U.S. DEPT. OF COMM./PAT. & TM-PTO-435L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
		Primary Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
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